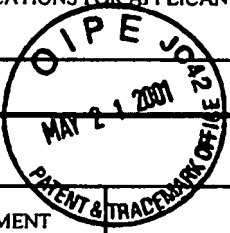


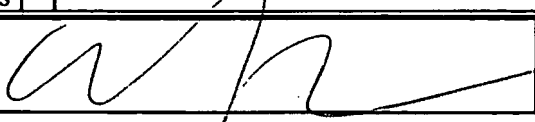
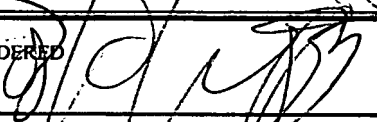
FORM PTO-1449 (Modified)				ATTY. DOCKET NO. YOR9200010367		SERIAL NO.: 09/847776	
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT				APPLICANT: Erik R. Altman et al			
(Use several sheets if necessary)				FILING DATE: May 2, 2001		GROUP:	



REFERENCE DESIGNATION										U.S. PATENT DOCUMENTS									
EXAMINER INITIALS		DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)					
	AA																		
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	AM																
	AN																
	AO																
	AP																

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)		
g	AQ	IEEE Computer March 2000 "Dynamic and Transparent Binary Translation" M. Gschwind et al pp. 54-59
h	AR	ACM/IEEE 32nd Intl. Symposium on Microarchitecture, " Optimizations and Oracle Parallelism with Dynamic Translation" K. Ebcioglu et al
i	AS	ACM/IEEE Computer Sigplan Notices Vol. 22, #7 July 1987 "Mimic: A Fast System/370 Simulator" Cathy May

EXAMINER 	DATE CONSIDERED 
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 (Modified)	ATTY. DOCKET NO. YOR920010367	SERIAL NO.:
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	APPLICANT: Erik A. Altman et al	
(Use several sheets if necessary)	FILING DATE: Herewith	GROUP:

jc0852 U.S. PTO
09/04/776
05/02/01

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
h	AA-6 2 0 2 1 3 2	03/13/01	Nayeem Islam et al	711	141	12/29/99
	AB					
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	AK					

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
					YES NO
AL					
AM					
AN					
AO					
AP					

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

2	AQ	M. K. Gschwind, "Method and apparatus for determining branch addresses in programs generated by binary translation" Research Disclosure, Vol. 41, #416, article 41698, Dec. 1998
2	AR	Computer IEEE 1993 "An Architectural Framework for Supporting Heterogeneous Instruction-Set Architectures" Gabriel M. Silberman et al pp 39-56, June 1993
	AS	

EXAMINER	DATE CONSIDERED
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